

EAST - [083411b:1]

File View Edit Tools Window Help

Drafts

Pending

Active

- L1: (592698) remov\$5 or replac\$5) same (path or graph or edge or weight or timing
- L2: (52) slack\$3 near9 graph
- L3: (34) skew same delay same graph
- L4: (18211) edge and path and timing and weight\$3
- L5: (0) 11 and 12 and 13 and 14
- L6: (9) 11 and 12 and 14**

Failed

Saved

- (52) slack near9 graph
- (52) slack\$3 near9 graph
- (34) skew same delay same graph
- (18211) edge and path and timing and weight\$3

Search DBs

All hit terms initially

11 and 12 and 14

	U	I	Document ID	IP	Title	Current	Current XRef
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030163797	29	System and method for identifying and eliminatin	716/13	716/12;
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6594806 B1	22	System and method for performing timing analysis	716/6	
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6209123 B1	21	Methods of placing transistors in a circuit layo	716/14	716/11;
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6099583 A	25	Core-based placement and annealing methods for p	716/16	716/10;
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6086631 A	25	Post-placement residual overlap removal method f	716/16	716/17;
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6006024 A	11	Method of routing an integrated circuit	716/12	
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5987086 A	11	Automatic layout standard cell routing	716/1	
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5984510 A	11	Automatic synthesis of standard cell layouts	716/2	716/9
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5710711 A	11	Method and integrated circuit adapted for partia	716/20	

Inbox... United... 1999... http://... PALM... sc0834... 60995... EAS... EAST...